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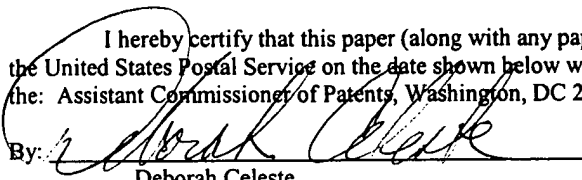
February 4, 2003

Assistant Commissioner for Patents
Washington, DC 20231

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner of Patents, Washington, DC 20231 on:

By:


Deborah Celeste

February 4, 2003
Date

**RE: Power of Attorney
Shipley Company, LLC**

Dear Sir:

Enclosed herewith are the following documents:

1. Power of Attorney with Appendix A; and
2. Return Postcard.

Please charge Deposit Account 50-0369 for any fees that may be required.

Very truly yours,

By:



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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
POWER OF ATTORNEY**

Serial No.: Attached Appendix A

Name of Assignee: Shipley Company, L.L.C.
Address: 455 Forest Street
Marlborough, MA 01752

TO THE ASSISTANT COMMISSIONER FOR PATENTS

Honorable Sir:

I hereby appoint:

Maria M. Eliseeva	Reg. No. 43,328	David D. Lowry	Reg. No. 38,538
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as attorneys to prosecute this application, to transact all business in the Patent and Trademark Office connected with the above-referenced patent application. Applicant hereby revokes all previous powers of attorney associated with this application.

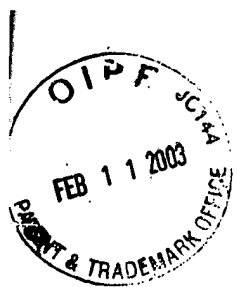
Please direct all future correspondence to the principal attorney of record as:

Maria M. Eliseeva
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Shipley Company, L.L.C.

By: Darryl P. Frickey
Darryl P. Frickey
Assistant Secretary and Director of Patents
Shipley Company, L.L.C.

Dated: January 27, 2003



APPENDIX A

<u>Title</u>	<u>Serial Number</u>	<u>Filing Date</u>
Optical Connector System	10/054,575	November 13, 2001
Optical Device Package for Flip-Chip Mounting	10/013,084	December 10, 2001
Optical Device Package for Flip-Chip Mounting	10/218,104	August 12, 2002
Structure for Aligning Chips in Stacked Arrangement	10/013,773	December 13, 2001
Structure for Aligning Chips in Stacked Arrangement	10/218,105	August 12, 2002
Structure for Aligning Chips in Stacked Arrangement	10/015,271	December 13, 2001
Optical Switch Assembly with Flex Plate and Method for Making	10/022,726	December 20, 2001
Method and Device for Passively Locating a Fiber Stub in a Groove	10/071,871	February 8, 2002
Method for Making Optical Devices With a Moving Mask and Optical Devices Made Thereby	10/081,995	February 21, 2002
Combined Wet and Dry Etching Process for Micromachining of Crystalline Materials	10/071,261	February 7, 2002
Tapered Optical Fiber for Coupling To Diffused Optical Waveguides	10/113,817	March 28, 2002
Wet and Dry Etching Process on <110> Silicon and Resulting Structures	10/124,612	April 17, 2002
Single Mask Lithographic Process for Patterning Multiple Types of Surface Features	09/519,165	March 6, 2000

<u>Title</u>	<u>Serial Number</u>	<u>Filing Date</u>
Solder Pads and Method of Making a Solder Pad	10/126,491	April 19, 2002
Venting Optical Microbench	10/135,192	April 29, 2002
V-Groove with Tapered Depth And Method for Making	10/067,218	February 7, 2002
Optoelectronic Submount Having an On-Edge Optoelectronic Device	10/066,299	January 31, 2002
Micromachined Structures Made by Combined Wet and Dry Etching	10/076,858	February 14, 2002
Inductive Magnetic Recording Head Having Inclined Magnetic Read/Write Pole and Method of Making Same	09/560,418	April 27, 2000
Optical Assembly for Coupling with Integrated Optical Devices and Method For Making	09/987,766	November 15, 2001
Optical Device Package	09/966,973	September 28, 2001
Submount Having Transmission Line and Method for Forming	09/987,139	November 13, 2001
Optical Fiber Attached to a Substrate	10/126,938	April 19, 2002
Optical Fiber with Collimated Output Having Low Back Reflection	10/041,517	October 18, 2001
Optical Device Package Having a Configured Frame	09/990,509	November 21, 2001
Optical Bench with Alignment Subassembly	09/988,055	November 16, 2001
Two-Dimensional Optical Fiber Array Made from Etched Sticks Having Notches	09/615,101	July 13, 2000

<u>Title</u>	<u>Serial Number</u>	<u>Filing Date</u>
Optical Fiber Array for Preventing Flow of Glue Between Fibers and Waveguide	09/713,117	November 15, 2000
Optoelectronic Packages Having Insulation Layers	09/824,205	April 3, 2001
Methods and Devices for Coupling Optoelectronics Packages	09/827,183	April 6, 2001
Open Face Optical Fiber Array For Coupling to Integrated Optic Waveguides and Optoelectronic Submounts	09/614,155	July 11, 2000
Fiber Optic Array Switch	09/833,282	April 12, 2001
Optoelectronic Device-Optical Fiber Connection having Micromachined Pit for Passive Alignment of the Optoelectronic Device	10/192,752	July 10, 2002
Wafer Level Packaging for Optoelectronic Devices	09/999,517	October 24, 2001
Single-Piece Alignment Frame For Optical Fiber Arrays	09/825,990	April 5, 2001
Single-Piece Alignment Frame For Optical Fiber Arrays	10/208,090	July 30, 2002
Fiber Array with V-Groove Chip And Mount	10/037,971	October 24, 2001
Microchip Having Grayscale and Micromachined Features and Single Mask Process for Patterning Same	09/999,332	October 26, 2001
Multi-Level Lithography Masks	09/853,250	May 9, 2001
Embedded Waveguide with Alignment Grooves and Method for Making Same	09/978,804	October 15, 2001

Title

Serial Number

Filing Date

Variable Width Waveguide for Mode-
Matching and Method for Making

09/983,984

October 26, 2001

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